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						REVISIONS					
н	F	REV.	ECO. N	0	DESCRIPTION					BY	1
J 3067					UPDATE .031 PCB TOLERANCE, ADD MM DIMENSIONS					JHSU	1
	К 3179			AD B	ADD 1mm THICK PCB DIMENSIONS, REMOVE BETWEEN-POSITION KEY FOR 1mm PITCH				9/22/2014 3/27/2015		
	L 3656				ADDED NOTES 1, 2, 3, 4 & DETAIL A, UPDATE RECOMMENDED PCB OVERALL LENGTH				4/14/2017	ТТ/ЈН	
					RECOMMENDED PCB OVERALL LENGTH						
											-
03	31±.00	6[0.79	±0.15] ()R							
		-	±0.15] (_	_					
.06	52±.00	8[1.57]	±0.20] ()R	.010	0.25] RE	F /	1 🗆 Г	7		
.09	93±.00	8[2.36]	±0.20] ()R	(SEE	NOTE 1)	(E
.12	25±.00	8[3.18	±0.20]			-	<u> </u>				
_					TIE BAR		Å	_X			
NOT RECOMMENDED DETAIL A											
(SEE NOTE 2)											
\	010 ±	.005[0	.25±0.1	3] (FOR	.031[0.79]	PCB)					
		-		- •	.039[1.00]						
	.020 ±	.010[0	.51±0.2	5] (FOR	.062[1.57]	PCB)					
.051±.010[1.30±0.25] (FOR .093[2.36] PCB)											D
-	.083	.010[2	.11±0.2	5] (FOR	.125[3.18]	PCB)					
					TABLE 1:						ך ואר
					CONTACT CENTERS						
					CONTAC	I CENTERS	· · · · ·	[10.95]		15.49]	1
					THOL						
MAX [INCH 0.039	MM 1.00	INCH N/A	MM N/A	INCH 0.033	MM 0.84	
					0.059	1.00	N/A	N/A	0.033	0.84	- C
					0.100	2.54	0.080	2.03	0.070	1.78	11
					0.125	3.18	0.080	2.03	0.070	1.78	
					0.150	3.81	0.080	2.03	0.070	1.78	
≜					0.156 * DDESS	3.96	0.100	2.54		1.78	
	. 010						_		NS, .045[1.1 R CONNECT		-
DEPTH	+ .010	v[U.25]			•	ERE. PLEAS	•				
L L		TABL	E 2:								
¥					D	•			POSITIONS)	
			ONTACT C	ENTERS	(TOLERANCE: ±.002[0.05]) .431[10.95] INSULATOR HEIGHT .610[15.49] INSULATOR HEIGHT						
EEN POS		5) <u> </u>	NCH	MM	INCH		MM	.610[15.4 INCF		MM	. В
		0	.050	1.27	N/A	1	N/A	0.03)	0.76	
.Y)			.100	2.54	0.040		02	0.05		1.27	
-			.125	3.18 3.81	0.065		65 02	0.07		1.91 1.22	
			.156	3.96	0.040		02	0.048		1.22	
* PRESS FIT, .031[0.79] x .062[1.57] PINS, .045[1.14] SQ. PINS, HIGH PROFILE MBB, AND ALL											
					LISTED HERE					—	
											_
	UNLESS	OTHERWIS	SE SPECIFIE	_	RAWN DATE NAME 8/15/2011 JH ESULLINS						
			N INCHES [M		ATION HEREIN CONTAINS	CONNECTOR SOLUTIONS					╡.
TOLERANCES: ANGULAR: ± 30' SULLINS EL XX=+ 02 [5] SULLINS EL					ARY INFORMATION OF LECTRONICS AND IS NOT PRODUCED, USED OR RECOMMENDED MATING PCB LAYOUT						· A
		.XXX=± .00	5 [.13]	PURPOSE E AUTHORI	ixcept as specifically PART NUMBER						\neg
					SULLINS ELECTRONICS. N/A			RE	$\overline{\mathbf{v}}$		
τανιτ		TOLERAN	TOLERANCING				453		900		<u> </u>
IANT PER: ASME Y14.5M-2009						SCALE: 4:1			SHEET 1 OI	F 1	
4	I I		2		1	0		I	1		

FILE NAME: 900, RECOMMENDED MATING PCB LAYOUT

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